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CONFIRMATION NO. 5441

Bib Data Sheet

| | | | | |
|-----------------------------|-----------------------------------|--------------|------------------------|---------------------------------|
| SERIAL NUMBER 10/808,341 | FILING DATE 03/25/2004 RULE | CLASS 711 | GROUP ART UNIT 2189 | ATTORNEY DOCKET NO. 101-1019 |
|-----------------------------|-----------------------------------|--------------|------------------------|---------------------------------|

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** CONTINUING DATA *****

Non file 5/16/06

** FOREIGN APPLICATIONS *****

REPUBLIC OF KOREA 2003-58781 08/25/2003 NO
wlrc

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/04/2004

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|---------------------------------|---|--|------------------------|-----------------------|----------------------------|
| Foreign Priority claimed | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR COUNTRY KOREA, REPUBLIC OF | SHEETS DRAWING 8 | TOTAL CLAIMS 16 | INDEPENDENT CLAIMS 6 |
| 35 USC 119 (a-d) conditions met | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>wlrc</i> Initials | | | | |

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TITLE

DSP (digital signal processing) architecture with a wide memory bandwidth and a memory mapping method thereof

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